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<p>O.I.P.E.</p> <p>SCANNED   ③ G.A. </p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/895997	CONT/PRIOR F	CLASS <del>25T</del> 713	SUBCLASS 410	ART UNIT <del>28TT</del> <del>2820</del> 2115	EXAMINER. <del>LEE</del> Cao
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APPLICANTS Shinya Hasegawa

TITLE Semiconductor integrated circuit and functional block of the same

PTO-2040  
12/89

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____  _____	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
	_____  _____  _____  _____ (Primary Examiner) (Date)		<b>ISSUE FEE</b>	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		<b>ISSUE BATCH NUMBER</b>	
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